

AMENDMENTS

In the Claims

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Please cancel claims 3, 14, 20, and 21 without prejudice, waiver, or disclaimer.

Please substitute the following clean copy text for the pending claims of the same number.

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1. (Twice Amended) An apparatus performing the addition of a PKG recoded number, said apparatus comprising:
a circuitry configured to receive at least a first value and a second value; and
a first adder configured to add said first value and said second value, wherein said second value is at least one of a P value, a K value, and a G value of a PKG recoded number and said first adder generates a carry-out value and at least one of a P value, a K value and G value of a PKG recoded number wherein said circuitry generates a sum value and a carry value.

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4. (Once Amended) The apparatus of claim 1, wherein said circuitry further comprises:
a second adder configured to add said PKG recoded number from said first adder and a carry-in value.

5. (Twice Amended) The apparatus of claim 1, wherein said first value is at least one of a P value, a K value and a G value of a PKG recoded number.

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7. (Twice Amended) A method for performing the addition of PKG recoded numbers, comprising:

receiving a first value;

receiving a second value, wherein said second value is at least one of a P value, a K value, and a G value of a first PKG recoded number; and

generating a sum value including at least one of a P value, a K value and G value of a second PKG recoded number and a first carry-out value from said first value and said second value.

8. (Twice Amended) The method of claim 7, further comprising:

forwarding a first result including at least one of a P value, a K value and a G value from said generating.

9. (Twice Amended) The method of claim 8, further comprising:

adding said first result and a carry-in value;

generating a final sum value from said adding; and

generating a final carry-out value from said adding.

12. (Twice Amended) The method of claim 7, further comprising:

converting at least one dual rail encoded value into said second value.

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13. (Twice Amended) An apparatus for apparatus performing the addition of PKG recoded number, said apparatus comprising:

means for receiving a first value;

means for receiving a second value, said second value including at least one of a P value, a K value and G value of a PKG recoded number ; and

means for generating a first sum value including at least one of a P value, a K value and G value of a second PKG recoded number and a first carry-out value from said first value and said second value.

B 15. (Twice Amended) The apparatus of claim 13, further comprising:
means for adding said first sum value and a carry-in value to generate a final
sum value and a final carry-out value.

19. (Twice Amended) An apparatus performing the addition of PKG recoded
numbers, said apparatus comprising:
a circuitry configured to receive at least one of a P value, a K value and a G
value of a first PKG recoded number and at least one of a P value, a K value and a G
value of a second PKG recoded number; and
a first carry save adder configured to generate a first carry-out value and a first
result comprising at least one of a P value, a K value and a G value.

B 22. (Newly Added) The apparatus of claim 19, further comprising:
a second carry save adder configured to receive the first result and a carry-in
value, the second carry save adder configured to generate a sum value and a final
carry-out value.